

CLAIMS:

1. A resistor network (2) such as a resistor ladder network, comprising at least a resistor body (4) which is provided with at least a column (6) of taps (8) situated between a first tap and a second tap, wherein, in use, at least two taps can be connected with respective first and second sources of reference input potentials, and wherein each tap of the at least one column of taps can be used for outputting an output potential via a contact area which is connected with the concerning tap, characterised in that, the resistor body (4) comprises a multiple of resistor sub-bodies (5), wherein each resistor sub-body is connected with a column (6) of taps (8), and wherein the only electrical connections between the resistor sub-bodies (5) are electrical connections via taps (8) connected with the resistor sub-bodies (5).

2. A resistor network (2) according to claim 1, characterised in that, each resistor sub-body (5) is provided with at least one column (6) of taps (8), wherein each tap (8) of the column (6) of taps (8) is an extremity such as a T-shaped or S-shaped projection which is connected with the concerning resistor sub-body (5).

3. A resistor network (2) according to one of the preceding claims, characterised in that, a plurality of taps (8) of a first column (6) of taps (8) of a first resistor sub-body is connected with a plurality of taps (8) of a second column of taps of a second resistor sub-body, wherein each tap (8) of the plurality of taps (8) of the first column is connected with only one tap of the plurality of taps of the second column and wherein each tap (8) of the plurality of taps (8) of the second column is connected with only one tap of the plurality of taps of the first column.

4. A resistor network according to claim 3, characterised in that, each tap (8) of the first column is shifted at least one column position with respect to the column position of the tap of the second column with which the tap of the first column is connected.

5. A resistor network according to claims 3 or 4, characterised in that,

the respective connections between the resistor sub-bodies with the taps (8) are made during fabrication of the resistor network at different arbitrary positions with respect to the respective taps (8).

- 5 6. A resistor network (2) according to one of the preceding claims, characterised in that, the resistor network (2) is a semiconductor-circuit, wherein each resistor sub-body comprises a number of resistor layers, wherein each semi-conducting resistor layer comprises at least two taps, and wherein the semi-conducting resistor layers are interconnected via the taps.

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7. Analog-Digital converter for generating a digital output signal on the basis of an analog input signal, characterised in that, the Analog-Digital converter comprises a resistor network (2) according to one of the preceding claims.

- 15 8. Digital-Analog converter for generating an analog output signal on the basis of a digital input signal, characterised in that, the Digital-Analog converter comprises a resistor network (2) according to one of the claims 1-6.

9. Method for manufacturing a resistor network (2) such as a resistor ladder
20 network, comprising the following step:
- manufacturing a resistor network (2) comprising a resistor body (4) with at least a column (6) of taps (8), wherein the taps (8) are situated between a first tap and a second tap, characterised in that, the method also comprises the following steps:
 - generating a number of resistor sub-bodies (5) for shaping the resistor body (4), wherein
25 the resistor sub-bodies (5) are mutually electrically insulated, and wherein each resistor sub-body (5) is connected with at least a column (6) with taps (8);
 - electrically connecting a plurality of taps of a first column of taps of a first resistor sub-body with a plurality of taps of a second column of taps of a second resistor sub-body, wherein each tap of the plurality of taps of the first column is connected with only one tap
30 of the plurality of taps of the second column and wherein each tap of the plurality of taps of the second column is connected with only one tap of the plurality of taps of the first column.

10. Method for manufacturing a resistor network (2) according to claim 9, characterized in that, each top of the first column is shifted at least one column position with respect to the column position of the second column with which the top of the first column is connected.

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11. Method of manufacturing a resist network (2) according to claim 9 or 10, characterised in that the connection with the tops are made at different arbitrary positions.